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Patent Application Serial No	10/609,31 ⁻
Filing Date	
Inventorship	
Assignee	Micron Technology, Inc
Group Art Unit	
Examiner	
Attorney's Docket No	MI22-2236
Title: Semiconductor Processing Patterni	ing Methods and Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. Copies of the cited art are not included (1276 Off. Gaz. Pat. Off 55, 05 August 2003). No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action on the merits, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Reg. No. 46,791

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FILING DATE June 26, 2003 **GROUP** 2811

*Examiner's Initials		Document Number	Date	Name 	Class	Subclass	Filin If App	g Date ropriate
	AA	6,753,618	06/2004	Basceri et al.	257	915		
	AB	5,470,794	11/1995	Anjum et al.	437	200		
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